ES2C0-15 Analogue Electronic Design

25/26

Department School of Engineering Level Undergraduate Level 2 Module leader Ryan Montague Credit value 15 Module duration 10 weeks Assessment 50% coursework, 50% exam Study location University of Warwick main campus, Coventry

Description

Introductory description

Analogue electronics

Module web page

Module aims

Analogue electronics is one of the core requirements for any electronics and electrical engineer. The module is designed to provide learning on the practical applications of different analogue electronic circuits and devices that are commonly used in modern electronics.

There four fundamental categories of components that are critical for any electronics or electrical engineer to understand. These are:

- Linear devices;
- Active/passive filters;
- Voltage Regulation/reference;
- Signal conversion.

This module will present tools and techniques of used combinations of these components to create a useful analogue electronic system.

Professional Engineers must appreciate the concept of electronic analogue systems and

how they interact with digital systems.

Outline syllabus

This is an indicative module outline only to give an indication of the sort of topics that may be covered. Actual sessions held may differ.

Lecture Topics include:

- Diodes, BJTs and FETs transistors as individual devices and in use as simple linear amplifiers. In addition, use of transistors as high/low sided switches and level shifters.
- Analogue Circuit Modelling and Simulation: Schematic capture and simulation of analogue electronic components.
- Voltage regulators (including LDOs), voltage references and current sources. How to construct and deploy these components.
- Digital to Analogue and Analogue to Digital Converters, including SAR, Sigma-delta and flash
- Operational Amplifiers (including internal topology), covering different circuit configuration with focus on single supply (5V, 3.3V and 1.8V) operation. This will include instrumentation amplifiers and comparators
- Sources of electrical noise, passive and active filters, covering operation and design methods
- Signal Selection, processing and conversion (including multiplexing, ADC and DAC converters)
- Analogue oscillators, waveform generation and timers
- Non-linear circuits, including log amplifiers

Learning outcomes

By the end of the module, students should be able to:

- Appreciate a range of different analogue electronic devices and circuits
- Design analogue electronic systems to fulfil specifications. [C1, M1 C2, M2, C3, M3, C4, M4(D)]
- Measure analogue electronic components, calculate, design and build analogue electronic circuits to determine performance and behaviours. [C12, M12, C13, M13]
- Analyze and calculate operating states of common analogue circuits [C1, M1, C2, M2, C3, M3, C4, M4(D)]

Indicative reading list

Microelectronics Circuit Analysis and Design, Donald Neaman, 4e 2009.

Subject specific skills

Ability to apply relevant practical and laboratory skills building and testing analogue electronic

circuits

Transferable skills

Numeracy: apply mathematical, computational and modelling methods in an engineering context

Study

Study time

Туре	Required 20 sessions of 1 hour (13%)	
Lectures		
Seminars	2 sessions of 1 hour (1%)	
Demonstrations	2 sessions of 1 hour (1%)	
Practical classes	5 sessions of 3 hours (10%)	
Private study	111 hours (74%)	
Total	150 hours	

Private study description

111 hours of guided independent learning

Costs

No further costs have been identified for this module.

Assessment

You must pass all assessment components to pass the module.

Assessment group C3

	Weighting Study time	Eligible for self-certification
LABORATORY RESULTS REPORTING	50%	No
ONLINE QMP LABORATORY RESULTS	REPORTING	
Online Examination	50%	No
QMP online examination		
~Platforms - AEP,QMP		

- Online examination: No Answerbook required
- Students may use a calculator
- Engineering Data Book 8th Edition
- Graph paper

Feedback on assessment

- Support through advice and feedback hours.
- Cohort-level feedback on QMP Lab Quiz
- Cohort-level feedback on final exam.

Past exam papers for ES2C0

Availability

Courses

This module is Core for:

- Year 2 of UESA-H63W BEng Electronic Engineering
- Year 2 of UESA-HH35 BEng Systems Engineering
- Year 2 of UESA-H612 Computer Systems Engineering MEng
- Year 2 of UESA-H163 MEng Biomedical Systems Engineering
- Year 2 of UESA-H63X MEng Electronic Engineering
- UESA-HH31 MEng Systems Engineering
 - Year 2 of HH31 Systems Engineering
 - Year 2 of HH35 Systems Engineering
- Year 2 of UCSA-G404 Undergraduate Computer Systems Engineering
- Year 2 of UESA-H605 Undergraduate Electrical and Electronic Engineering
- Year 2 of UESA-H606 Undergraduate Electrical and Electronic Engineering MEng

This module is Optional for:

- Year 2 of UESA-H113 BEng Engineering
- Year 2 of UESA-H114 MEng Engineering
- h112